

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Patent Application of:	)	
	)	
Roussel et al.	)	Examiner: Not Yet Assigned
	)	
Application No.: Not Yet Assigned	)	Art Unit: Not Yet Assigned
	)	
Filed: Concurrently Herewith	)	
	)	
For: Staggering Execution of a Single	)	
Packed Data Instruction Using the	)	
Same Circuit	)	

Commissioner for Patents  
P.O. Box 1450  
Alexandria, Virginia 22313-1450

INFORMATION DISCLOSURE STATEMENT

Sir:

Enclosed is a copy of Information Disclosure Citation Form PTO-1449 or PTO/SB/08 together with copies of the documents cited on that form. Certain references listed on the enclosed Form PTO-1449 were previously identified in the parent application of the present application, and copies of the references were furnished at that time. Accordingly, additional copies of the references are not submitted herewith, so as not to burden the file with duplicate copies of references. It is respectfully requested that the cited documents be considered and that the enclosed copy of Information Disclosure Citation Form PTO-1449 or PTO/SB/08 be initialed by the Examiner to indicate such consideration and a copy thereof returned to applicant(s).

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Pursuant to 37 C.F.R. § 1.97, the submission of this Information Disclosure Statement is not to be construed as a representation that a search has been made and is not to be construed as an admission that the information cited in this statement is material to patentability.

Pursuant to 37 C.F.R. § 1.97, this Information Disclosure Statement is being submitted under one of the following (as indicated by an "X" to the left of the appropriate paragraph):

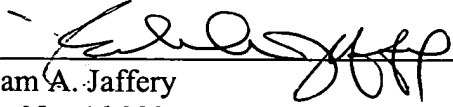
- ☒ 37 C.F.R. §1.97(b).
- ☐ 37 C.F.R. §1.97(c). If so, then enclosed with this Information Disclosure Statement is one of the following:
- ☐ A statement pursuant to 37 C.F.R. §1.97(e) or
- ☐ A check for \$180.00 for the fee under 37 C.F.R. § 1.17(p).
- ☐ 37 C.F.R. §1.97(d). If so, then enclosed with this Information Disclosure Statement are the following:
- (1) A statement pursuant to 37 C.F.R. §1.97(e); and
  - (2) A check for \$180.00 for the fee under 37 C.F.R. §1.17(p) for submission of the Information Disclosure Statement.

If there are any additional charges, please charge Deposit Account No. 02-2666.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Dated: October 20, 2003

  
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I hereby certify that this correspondence is being deposited with the United States Postal Service as Express mail with sufficient postage in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450 on October 20, 2003.

(Date of Deposit)

Leah Schwenke

(Typed or printed name of person mailing correspondence)



(Signature of person mailing correspondence)

<b>US Department of Commerce</b> <b>Patent and Trademark Office</b>  <b>Form PTO-1449 (Modified)</b>			<b>Atty. Docket No.:</b> 42390P4920C4		<b>Application No.:</b> Not Yet Assigned		
			<b>Applicant:</b> Roussel et al.				
			<b>Filing Date:</b> Concurrently Herewith				
<b>US Patent Documents</b>							
<b>Examiner's Initials</b>		<b>Date</b>	<b>Document Number</b>	<b>Name</b>	<b>Class</b>	<b>Sub- Class</b>	<b>Filing Date</b>
		04/12/94	5,303,355	Gergen et al.			
		06/09/98	5,765,037	Morrison et al.			
		02/25/97	5,606,677	Balmer et al.			
		01/23/01	6,178,482	Sollars			
		02/09/99	5,870,619	Wilkinson et al.			
		05/15/01	6,233,671	Abdallah et al.			
		09/19/00	6,122,725	Roussel et al.			
		02/20/01	6,192,467	Abdallah et al.			
		05/08/01	6,230,253	Roussel et al.			
		12/31/02	6,502,117	Golliver et al.			
		03/09/99	5,880,983	Elliott et al.			
		05/08/01	6,230,257	Roussel et al.			
<b>Foreign Patent Documents</b>							
<b>Examiner's Initials</b>		<b>Date</b>	<b>Document Number</b>	<b>Country</b>	<b>Class</b>	<b>Sub- Class</b>	<b>Translation</b>
<b>Other Documents (Including Author, Title, Date, Pertinent Pages, etc.)</b>							
<b>Examiner</b>			<b>Date Considered</b>				

Examiner: Initial if reference is considered, whether or not citation is in conformance with MPEP 609; Draw a line through the citation if not in conformance and not considered. Include a copy of this form with the next communication to the applicant

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		03/21/00	6,041,403	Parker et al.	712	210	09/27/96
		01/25/00	6,018,351	Mennemeier et al.	345	437	10/30/97
		12/26/89	4,890,218	Bram	364	200	08/10/88
		09/30/97	5,673,427	Brown et al.	395	595	07/03/96
		12/22/98	5,852,726	Lin et al.	395	376	12/19/95
		03/02/76	3,941,990	Rabasse	235	176	01/02/74
		09/17/85	4,542,476	Nagafuji	364	749	08/03/82
		08/19/75	3,900,724	McIver et al.	235	164	02/11/74
<b>Foreign Patent Documents</b>							
<b>Examiner's Initials</b>		<b>Date</b>	<b>Document Number</b>	<b>Country</b>	<b>Class</b>	<b>Sub- Class</b>	<b>Translation</b>
		10/28/99	GB 9907221.7	Great Britain	G06F	9/302	
		03/06/97	WO 97/08608	PCT	G06F	7/00	
		06/26/97	WO 97/22923	PCT	G06F	9/34	
		06/26/97	WO 97/22924	PCT	G06F	9/34	
		06/26/97	WO 97/22921	PCT	G06F	9/00	
<b>Other Documents (Including Author, Title, Date, Pertinent Pages, etc.)</b>							
		gnu@gnu.org (ChangLog) pages A-D.					
		Abbott et al., "Broadband Algorithms with the Micro Unity Mediaprocessor", Proceedings of COMPCON 1996, 349-354.					
		Hayes et al., "MicroUnity Software Development Environment", Proceedings of COMPCON 1996, 341-348.					
		"64-Bit and Multimedia Extensions in the PA-RISC 2.0 Architecture", Helett Packard, downloaded from Website rblee@cup.hp.com.huck@cup.hp.com, pages 1-18.					
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				<b>Date Considered</b>			

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		06/24/97	5,642,306	Mennemeier et al.			
		02/24/98	5,721,892	Pelog. et al.	712	221	11/06/95
		05/10/94	5,311,508	Buda et al.	370	60	12/23/91
		03/27/73	3,723,715	Chen et al.	235	175	08/25/71
		08/10/99	5,936,782	Fischer et al.	364	754.03	12/20/95
		08/11/98	5,793,661	Dulong et al.	364	750.5	12/26/95
		05/11/93	5,210,711	Rossmere et al.	364	786	02/26/92
		06/20/95	5,426,598	Hagihara	364	759	02/22/94
		07/04/72	3,675,001	Singh	235	175	12/10/70
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<b>Other Documents (Including Author, Title, Date, Pertinent Pages, etc.)</b>							
		"TM 1000 Preliminary Data Book", Philips Semiconductors, 1997.					
		"21164 Alpha TM Microprocessor Data Sheet", Samsung Eleectronics, 1997.					
		"Silicon Graphics Introduces Enhanced MIPS ® Architecture to lead the Interactive Digital Revolution, Silicon Graphics", October 21, 1996, downloaded form the Website webmaster @ www.sgi.com, pages 1-2.					
		"Silicon Graphics Introduces Compact MIPS ® RISC Microprocessor Code for High Performance at a low Cost", October 21, 1996, downloaded from the Website webmaster @ www.sgi.com, pages 1-2.					
		Killian, Earl, "MIPS Extension for Digital Media", Silicon Graphics, pages 1-10.					
		"MIPS V Instruction Set", pp B1-B37.					
		"MIPS Digital Media Extension", pages C1-C40.					
		"MIPS Extension for Digital Media with 3D", MIPS Technologies, Inc., March 12, 1997, pages 1-26.					
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		"MIPS Digital Media Extension", pages C1-C40.					
		"MIPS Extension for Digital Media with 3D", MIPS Technology, Inc., March 12, 1997, Pages 1-26.					
		"64-Bit and Multimedia Extensions in the PA-RISC 2.0 Architecture", Helett Packard, downloaded form Website rblee@cup.hp.com.huck@cup.hp.com. Pages 1-18.					
		"The VIS TM Instruction Set", Sun Microsystems, Inc., 1997 pages 1-2.					
		"ULTRASPARC TM The Visual Instructions Set (VIStm): On Chip Support for New-Media Processing", Sun Microsystems, Inc., 1996, pages 1-7.					
		"ULTRASPARC TM and New Media Support Real-Time MPEG2 Decode with the Visual Instruction Set (VIS TM), Sun Microsystems, Inc., 1996, pages 1-8.					
		"ULTRASPARC TM Ultra Port Architecture (UPA): The New Media System Architecture, Sun Microsystems, Inc., 1996, pages 1-4.					
		"ULTRASPARC TM Turbocharges Network Operations on New Media Computing, Sun Microsystem, Inc., 1996, pages 1-5.					
		"The ULTRASPARC Processor-Technology White Paper, Sun Microsystems, Inc., 1995, 37 pages.					
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		"AMD-3D TM Technology Manual, Advanced Micro Devices, February 1998.					
		Hansen, Craig, "Architecture of a Broadband Mediaprocessor, Micro Unity Systems Engineering, Inc., 1996, pages 334-354.					
		Levinthal, Adam et al., "Parallel Computers for Graphics Applications, Pixar, San Rafael, CA., 1987, pages 193-198.					
		Levinthal, Adam et al. Porter, Thomas, "Chap-A SIMD Graphics Processor", Computer Graphics Project, Lucasfilm Ltd., 1984, pages 77-82.					
		Wang, Mangaser, Shrinivan, "A Processor Architecture for 3D Graphics Calculations, Computer Motion, Inc., pages 1-23.					
		Visual Instruction Set (VIS TM), User's Guide, Sun Microsystems, Inc., version 1.1 March 1997.					
		"ULTRASPARC TM Turbocharges Network Operations on New Media Computing, Sun Microsystem, Inc., 1996, pages 1-5.					
		"The ULTRASPARC Processor-Technology White Paper, Sun Microsystems, Inc., 1995, 37 pages.					
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